

## INTERNATIONAL SEARCH REPORT

International Application No.  
PCT/US2005/008373

A. CLASSIFICATION OF SUBJECT MATTER  
IPC 7 G06F12/08

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)  
IPC 7 G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	HASHIMOTO T ET AL: "A 90 MW MPEG-4 VIDEO CODEC LSI WITH THE CAPABILITY FOR CORE PROFILE" IEICE TRANSACTIONS ON ELECTRONICS, INSTITUTE OF ELECTRONICS INFORMATION AND COMM. ENG. TOKYO, JP, vol. E86-C, no. 7, July 2003 (2003-07), pages 1374-1384, XP001172046 ISSN: 0916-8524 abstract paragraphs '0001! - '2.4.2! ----- -/--	1-18

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

## \* Special categories of cited documents:

- \*A\* document defining the general state of the art which is not considered to be of particular relevance
- \*E\* earlier document but published on or after the international filing date
- \*L\* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- \*O\* document referring to an oral disclosure, use, exhibition or other means
- \*P\* document published prior to the international filing date but later than the priority date claimed

- \*T\* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
- \*X\* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
- \*Y\* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
- \*Z\* document member of the same patent family

Date of the actual completion of the international search

14 November 2005

Date of mailing of the international search report

25. 11. 2005

Name and mailing address of the ISA

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## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 6 272 597 B1 (FU JOHN WAI CHEONG ET AL) 7 August 2001 (2001-08-07) abstract figures 1,2 column 3, line 56 - column 3, line 61 column 4, line 6 - column 4, line 9 column 4, line 27 - column 6, line 10 column 7, line 41 - column 7, line 43 column 7, line 52 - column 7, line 57	19-34
X	EP 1 215 581 A (TEXAS INSTRUMENTS INCORPORATED; TEXAS INSTRUMENTS FRANCE) 19 June 2002 (2002-06-19) the whole document	19

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### Box II Observations where certain claims were found unsearchable (Continuation of item 2 of first sheet)

This International Search Report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

1. ☐ Claims Nos.:  
because they relate to subject matter not required to be searched by this Authority, namely:
2. ☐ Claims Nos.:  
because they relate to parts of the international Application that do not comply with the prescribed requirements to such an extent that no meaningful international Search can be carried out, specifically:
3. ☐ Claims Nos.:  
because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).

### Box III Observations where unity of invention is lacking (Continuation of item 3 of first sheet)

This International Searching Authority found multiple inventions in this international application, as follows:

see additional sheet

1. ☒ As all required additional search fees were timely paid by the applicant, this International Search Report covers all searchable claims.
2. ☐ As all searchable claims could be searched without effort justifying an additional fee, this Authority did not invite payment of any additional fee.
3. ☐ As only some of the required additional search fees were timely paid by the applicant, this International Search Report covers only those claims for which fees were paid, specifically claims Nos.:
4. ☐ No required additional search fees were timely paid by the applicant. Consequently, this International Search Report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.:

#### Remark on Protest

- ☐ The additional search fees were accompanied by the applicant's protest.
- ☒ No protest accompanied the payment of additional search fees.

FURTHER INFORMATION CONTINUED FROM PCT/ISA/ 210

This International Searching Authority found multiple (groups of) inventions in this international application, as follows:

1. claims: 1-18

An integrated circuit comprising:

- a processor core operable to perform data processing for the integrated circuit;
  - a cache memory operable to store data for the processor core; and
  - an on-chip memory operable to store data for the cache memory,
  - wherein the cache memory is filled with data from the on-chip memory for cache misses, and
  - wherein the on-chip memory is filled with data from an external memory under user control.
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2. claims: 19-34

An integrated circuit comprising a cache controller operable to handle memory transactions for a cache memory and a second memory at two different levels of a cached memory system, the cache controller including:

- a first address check unit coupled to a first processing unit and operable to determine whether a first memory address for a first memory access by the first processing unit is currently in the cache memory,
  - a second address check unit coupled to a second processing unit and operable to determine whether a second memory address for a second memory access by the second processing unit is currently in the cache memory, and
  - a logic unit coupled to the first and second address check units and operable to handle memory transactions for the first and second memory accesses by the first and second processing units.
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# INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/US2005/008373

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 6272597	B1	07-08-2001	
		AU 2220500 A	31-07-2000
		CN 1333906 A	30-01-2002
		DE 19983859 T0	28-02-2002
		GB 2359910 A	05-09-2001
		TW 454161 B	11-09-2001
		WO 0039764 A1	06-07-2000
EP 1215581	A	19-06-2002	NONE